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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/796351 Confirmation No.: 1989  
Applicant: ARVIDSON, *et al.*  
Filed: 9 March 2004  
TC/A.U.: 3653  
Examiner: Rodriguez, Joseph C.  
Docket No.: HSC27 DIV1  
Customer No.: 00137  
Date: 23 March 2006  
For: Method and Apparatus for Improving Silicon Processing Efficiency

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## AMENDMENT

Sir:

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4/6/06

In response to the final office action dated March 20, 2006 please amend the above-identified application as follows. The amendment is made to place the application in proper form for allowance. The applicants respectfully request entry of the amendment and allowance of the claims.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

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